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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10084943	FILING DATE 03/01/2002	CLASS 438	SUBCLASS	GAIL 2812	EXAMINER
<b>**APPLICANTS:</b> Kawakami Kazuya; Suzuki Yukihiro; Okutani Ken; Kajita Susumu; Hashimoto Takeshi;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>**FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2001-57974 03/02/2001					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 843.41231X00	
Verified and Acknowledged Examiners's initials TITLE : Method of manufacturing semiconductor integrated circuit device and semiconductor manufacturing apparatus					

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<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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